

# LVDC SERIES

LVDT/RVDT to Digital Converters Model Selection Guide

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	External Rafe	rence Supplied			
Model Sel					
Code	Signal Inputs	Reference in			
V 1	2 V IL @ 360-5000 H I.	2 V R M S			
V I	3 V L - L @ 368-5000 Hz.	3 V R M S			
V 3	7 V L-L @ 360-5000 H s.	TVRMS			
V 4	26V L-L @ 360-5000Hz.	26 V R M S			
V 5	26V L-L @ 360-5000Hz.	ILSVRM S			
V 6	26V L-L @ 360-5000H1.	12 V R M S			
V R	10 V L - L @ 400 H z.	16VRM S			
	CCC Reference P	ravided Internal			
Signal Inputs		Reference Out			
V M	JV L-L	3 V R M S @ 2500 H s.			
V 7	3 V L -L	7 V R M S @ 2540 H Z.			
V 8	7 V L-L	7 V R M S @ 1500 H r.			
V 9	7 V L-L	7 V R M S @ 400 H z.			
VA	26V L-L	16VRM S @ 2580Hr.			
V B	11.8 V L-L	26 VR M S @ 408Hz.			
V.F	4.36V L-L	TV VRM S @ 1880Hz. (3 W IR E)			
VH	12.25 V L-L	28 V R M S @ 880 H z. (2 W IR E)			
V N	5.5 V L-L	7 VM RS @ 1890H1. (2 W IR E)			
V P	.4476-1.1738VL-L	SVRMS & SKHI. (3 WIRE)			
V 5		3.9 V R M S @ 2500 H t. (3 W IR E)			
VТ		TVRMS @ 3200Hz. (RVDT 3 WIRE)			

Model Selection Guide:					
Use Basic Model # LVDC50	3 & 4 Wire LVDT 14 Bit Resolution				
LVDC40	3 & 4 Wire LVDT 12 Bit Resolution				
LVDC20	2 Wire LVDT 12 Bit Resolution				

#### Add:

- a) -V1 to -VZ for Model Code (see Table 2.2 above)
- b) -1 for Commercial Temp. 0-70C
  -2 for extended (Mil.) Temp. -55-+125C

example: Model: LVDC-50-V6-1 is a 14 bit LVDT/RVDT to Digital Converter, having a 3 VRMS @ 2500 Hz. reference output to .4VA load to drive the LVDT with a 3V L-L 3 or 4 Wire input.



## 3 £ 4 WIRE LVDT/RVDT TO DIGITAL SPECIFICATIONS

A 6.034 REV-A SHEET I OF 2

#### SPECIFICATIONS:

## ALL MODELS: @25°C

Resolution.... 14 Bits, +2 over/under range MSB's

Accuracy.... ±.15% of full scale ±1LSB

Repeatability..... +1LSB

Data Format..... Offset Binary, See table T.1

Over/Under Range...... 2 MSB's, 01 = Positive/over-range. 11 = Negative/under-range.

Frequency..... 360-5000Hz

(see options) 2-50VRMS ±10% available units, Reference-in.....

2, 3, 7 or 26VRMS ±10% standard options into 100K ohms.

Reference-Out..... (optional) 2-30VRMS ±10%, up to .4VA, 2, 3, 7 and 26VRMS ±10% standard options.

Signal Inputs..... 3 or 4 wire RVDT/LVDT format,

specified voltage input ±10% at 360 to 5000Hz.

Input impedance...... 70K ohms typical at 3 VRMS Transient Protect...... 100VDC max, w/o damage Acceleration.... (For a 1 LSB lag.) 124º/sec2 typ.,

= 34.4% of full scale/sec<sup>3</sup> typ. +5VDC @ .8 Amp. typ. +125 ma./channel +15VDC @ 25 ma./ channel, -15 @ 35 ma./channel Power Supplies.....

or, +12VDC @ 32 ma./channel, -12VDC @ 5 ma./channel

Temperature Range..... 0 degrees C to +70 degrees C on -1 units,

-55 degrees C to +105 degrees C -2 units

-55 degrees C to +125 degrees C Storage.....

Notes:

 Accuracy applies over operating temperature range, ±10% amplitude and frequency variations, & ±5% power supply variations.

Different input voltages and frequencies available.

Reference supply variations greater than 10% will cause a .1% additional error.

Faster settling times and higher rates available.

5) 883 Level B available on all units.



# LVDT/RVDT TO DIGITAL SPECIFICATIONS

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#### TABLE T1

BIT PATTERN

LVDT OUTPUT	(MSB) D15				(LSB) DO
+OVER TRAVEL	01	XXXX	XXXX	XXXX	xx
+FULL TRAVEL -1 LS	B 00	1111	1111	1111	11
+HALF TRAVEL	00	1100	0000	0000	00
+1 LSB	00	1000	0000	0000	01
NULL	00	1000	0000	0000	00
-1 LSB	00	0111	1111	1111	11
-HALF TRAVEL	0.0	0100	0000	0000	00
-FULL TRAVEL +1 LS	в 00	0000	0000	0000	10
-FULL TRAVEL	00	0000	0000	0000	00
-OVER TRAVEL	11	XXXX	XXXX	XXXX	XX

NOTE: 14 BIT RESOLUTION SHOWN. AT 12 BIT RESOLUTION, BITS DO AND D1 ARE ALWAYS 0.







