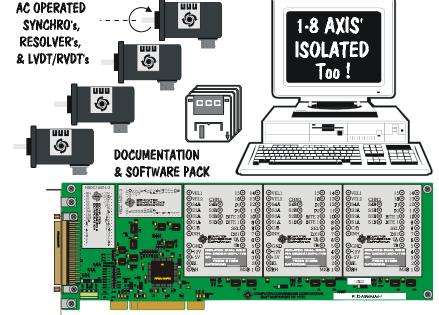


PCI-SYNCHROTM SERIES PCI 2.2 COMPLIANT SYNCHRO-RESOLVER-ENCODER-LVDT PCI BUS CARD OVERVIEW





Overview

The PCI-SynchroTM Series" product line are full size PCI 2.2 compliant synchro, resolver, and LVDTI/O cards, and resolver-sensor card sets used as absolute encoder systems.

These cards are ideally suited for both industrial and COTS military concerns using any PCI bus compatible computer for simulation, ATE, "Absolute" position sensing and motion control, coordinates & process control, navigation, GPS, data acquisition, radar, ships speed indicators, antenna & targetting applications.

The PCI-SynchroTM Series converters provide up to 4 and **8 channels** of **Isolated Synchro/Resolver LVDT/RVDT** or **absolute encoder** interface on a single, standard full size 2 PCI printed circuit card.

Computer Conversions' full line of standard, proven "whole" Synchro/Resolver input and output type converters, **CT's and CDX's etc...** are used to populate the same standard PCI compliant CCA's.

Differing **converters may be mixed** to minimize real estate of any type. Both industrial and COTS military grade (**extended**) **temperature** range versions are available.

100% Transformer **Isolation** is offered for **all** AC **I/O**, and various Isolated DC converters are available as standard product, **eliminating** concerns for

ground-loops, ground interjected (intermittent and ghostly) field noise, inductive surges, differing potentials, and high voltage field transients from effecting the card itself, the sensitive PCI bus backplane and any other device or system sharing these signals.

Maximum **versatility** has been employed on all PCI-SynchroTM products to assure the universal compatibility in adressing, timing, system, and specific computer **hardware**, **software**, **and backplane independence**.

All PCI-Synchro™ cards are configured with full plug and play capabilities, and are ideally suited to real-time applications because there is no PCI-Bus latency. The interface is a solidly-reliable/high-speed, true 32 bit "Long-Word-Level" register access.

Status registers are provided for various levels of **fault indication**, **Built-in-Test** (where applicable), and configuration criteria.

A sample demonstration program written in Visual C++ is provided with source code, which may be easily modified by the user for his/her particular application.

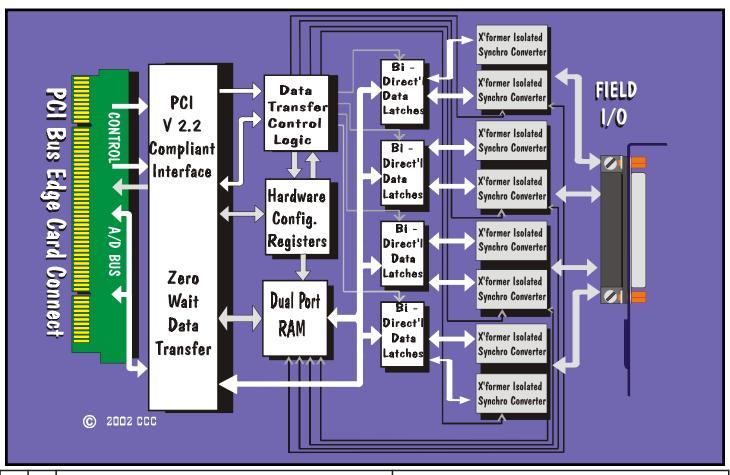
STANDARD I/O SELECTIONS Absolute Encoder Systems Synchro/Resolver To Digital Digital To Synchro/Resolver Syn/Res Control Transformers Multispeed & Multiturn Systems Synchro/Resolver Differentials Real-Time Control Differentials Absolute & Incremental Outputs DC Sin/Cos & Vector Generator's Digital Modulators & Demod s Active Coordinate Converters LVDT/RVDT Conversion

CONVERTER FUNCTIONS AND

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Isolated A/D's & D/A's



Base	Chan.		ADDRESS MAP FOR STATUS BITSIN THE HIGH WORD															AD	DRES	S MA	P FOI	R DAT	A BIT	S (LO	w wc	ORD)			LSB				
		31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0h	0	FLT	CP	WR														180	90	45													
04h	1	FLT	CP	WR														180	90	45													
08h	2	FLT	CP	WR												180	90		Data Bits, MSB Word Aligned			igned,	D0 = LSB =.0055 degrees										
0Ch	3	FLT	CP															180	90	45													
10h	4	FLT	CP													180	90	45	SE	SEE CHANNEL ADDRES			ORESS	SLEFT									
14h	5	FLT	CP															180	90	45													
18h	6	FLT	CP				©	Co	pyri	ght CCC 2001, 2002						180	90	45	Unsigned Integer, Binary Weig			Weigh	ted A	ngle									
1Ch	7	FLT	CP															180	90	45													
20h	CW														E2	E1	E0	ТВ	TA	ТВ	TA	ТВ	TA	ТВ	TA	R2	R1	R2	R1	R2	R1	R2	R1
		Not	used, s	haded	area =	Bits n	ot used	d, masl	k on re	eads, w	rite ze	ro's on	write		1 = E Exter	nable nal		Char 3 &		Char 2 &		Char 1 & :		Char 0 &		Chai 3 &	nnels 7	Char 2 &		Char 1 &		Char 0 &	
FLT	Input (S-D) Channels 1 = Loss of Signal/Reference & Overspeed., Writes = External Synchro Amplifier Built-In-Test Fault = 1							Amplifier Outputs, E0 = Channel 0 E1 = Channel 1				Models with -WS Option, = With Forced Angle Self Test:						Models With Programmable Resolution: R2 R1 = 00 = 10 Bits															
CP	1 = Cha	annel F	resent	Status	Repor	rt										Chan		TB TA = 00 = Normal Run Mode										01 = 10 0 = 10					
WR	1 = Wri	ite type	e Conv	erter, i	.e. Dig	ital to	Synchr	o, else	input	conver	er fund	ction i.e	e.S-D					TB TA = 01 = 90.0 Degrees Angle, +/-0.1 TB TA = 10 = 0.0 Degrees Angle, +/-0.1					R2 R1 = 11 = 16 Bits										
CW	CONT	ROL 1	Lwoi	RD De	tails																	egrees											

The demonstration program (with source code) is provided for **Out-Of-The-Box testing**, without any user programming required.

All cards may be user configured **completely Bus-Powered**, with **no external power supplies required**. Power source jumpers are provided to select the ± 12 VDC power source as

"Bus-Powered" or as external inputs via the onboard terminal block.

The use of "proven whole converter" modules assures the user of garaunteed system accuracy and dynamics ability, long term conversion integrity and stability.

All wiring is made through standard or 50 pin "D" style connectors.

Isolated D-A/ A-D Converters, Digital Modulators, Demodulators, AC

Reference Supplies, & related function converters may used with these cards.

The availability of synchro/resolver I/O on the PCI bus, allows the user to configure his systems with resident test ability, switched manual or automatic self test, and simulation type programs.





PCI-SYNCHROTM SERIES SYNCHRO, RESOLVER, & LVDT INPUT CHANNELS

Description

The **PCI-Synchro**TM cards facilitate up to 8 isolated input chanels, and are 1-8 continuously tracking synchro or resolver to **IBM PC/AT**TM card converters, employing type 2 ratiometric tracking converters for **high performance** applications.

They will accept any group of upto 8 individual, or 4 sets of paired multispeed; 3-wire **Synchro**, or 4-wire **Resolver** inputs, **or 2-4 wire LVDT/RVDT inputs**; over a frequency range of **50Hz. to 10KHZ**., and convert them into 10-16 bit words of natural binary data.

Data is addressable in a **long word 32 bit format** over the IBM PC/ATTM backplane. Data made available to the bus is continuously updated (tracking) without interruption; output **data is accurate, monotonic, and always fresh** up to the maximum tracking rate of the converter.

When the address is applied, and normal bus variables are set; the converters **data bits are latched simultaneously into separate buffered registers to prevent false reads.**

A Forced Self-test feature is optioned "-WS" that allows on program command an internal disconnect of inputs to read a 30° fixed analog test.

APPLICATIONS:

Antenna Monitoring
Closed Loop Servo Controls
Fire Control Systems
Avionic & Naval Systems
Conveyor Controls
Wind Speed Indicators
Machine Control Systems
Shaft Angle Encoding
Engine Test Stands
Material Handling Systems
High Speed Data Aquisition

FEATURES

Direct Synchro/Resolver/LVDT Inputs
Transformer Isolated I/O Options

High Speed, Stable, Ratiometric, Tracking Converters

High Noise Immunity/Insensitive ToAmplitude And Frequency Variations

No External Parts or Power Required On-Board Reference Supply Option's 10-20 Bit Resolution,& Multispeeds Incremental Encoder Output Options

True 16 Bit Word Level Interface Force Self-Test Option (-WS units)

A continuous Built-In Test (BIT) output representing the tracking mode, Loss of signals, Loss of Reference, for status report are provided for each input channel.

No external transformers, modules or signal conditioners are required. The synchro/resolver converters used feature internal solid-state or Transformer Isolated Scott T's that accept direct field voltage inputs.

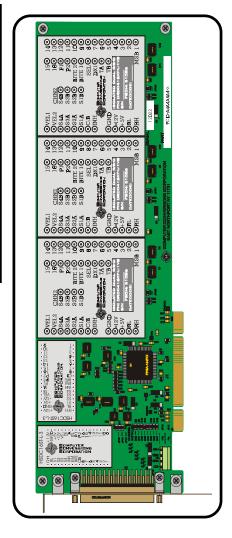
Isolation

Transformer isolated units are completely isolated from each other and the backplane for all the reference and signal lines.

This completely isolates the card and effectively the whole computer from all field wiring, and especially from any other device sharing these signals: eliminating concerns over; troublesome ground loops, ground induced noise, differing potentials, ground interjected spikes, and ghostly filed noise that so frequently takes down entire systems.

Bus Powered

No external supplies required! All units are available as completely bus powered via the PCI backplane



or as external inputs for the ± 12 VDC supplies. Power required is ± 12 , ± 3 , and ± 5 VDC, and the source is for the ± 12 VDC is strap selectable for power sourcing via the ISA backplane or as external power via the I/O connector.

Options currently available include: DC velocity outputs, internal reference supplies, quadrature pulse train outputs, high reliability and mil-grade extended temperature range units. Units with on-board DC-to-DC converters also available.

Additionally, PC/AT cards can be configured to meet particular OEM requirements. Many perspective custom applications can be configured with 100% standard product.

Specifications for all Input Cards/channels

_		SPECI	FICAT	IONS		
Resolution		10 Bits	12 Bits	14 Bits	16 Bits	18-20
Accuracy		+/-30'	+/-8.5'	+/-4'+1LSB	+/-4'	+/-1'
-GA Models				+/-4.5'+1LSB		
-HA Models		+/-21'		+/-2.7'	*+/-2.6'	+/-10sec.
-HAI Models					+/-40"+1LSB	
Tracking Rate	60Hz.	12.5	10	2.5	0.625	0.25
	400Hz.	40	40	10	2.5	1
	2.5Khz.+	100	80	30	5	1.2
-HS models	2.5KHz.+	200	200	50	10	
Acceleration		770	295	20		
	400Hz.	12600	4500	610	124	
for a 1 LSB lag	2,5KHz.	2500	9000	1620		
		1400	350	70		
	400Hz.	22000	5500	1100		
	2,5KHz.+	160K	40000	8100		
Step Responce	60Hz.	200ms.	360ms.	800ms.	1200ms.	
	2.5KHz.+	95ms.	95ms.	150ms.	600ms.	2000ms.
Frequency Range	60Hz.units	47-1000Hz.		400Hz.units 36	50 - 2000Hz.	
	2.5Khz. unit	s 2000-4.8K	hz.	Units to 10KH	z. available	
Reference Inputs			26VRM	S into 90K ohms		
			115VRM	S into 360K ohms	8	
Signal Inputs		11.8VRM	S L-L into 26	K ohms Minimum	L-L Balanced	
		26VRMS	L-L into 26k	Cohms Minimum	L-L Balanced	
		90VRMS	L-L into 2001	K ohms Minimum	L-L Balanced	
Breakdown (volts)		500 VD	C Minimum to	Ground on Trans	sformer Units	
Common Mode		8	0 Db. Minim	ım on Solid State	Units	
Power Supplies		+3VDC @	.4 Amp. typic	cal, +5 VDC @ 1	20 ma./channel	
	+12	2VDC @ 35	ma./channel, -	12VDC @ 45 m	a./channel (-12 u	ınits)
		or, +15VDC	25 ma./c	hannel, -15VDC	@35 ma./channe	1
Temperature	00	C to +60C or	n card level u	nits, OC to+70C o	on parts., (-1 uni	its)
(operating):	-400	C to +75C or	ard level un	its, -40C to +850	C on parts, (-3	units)
(Storage):			-550	C to +125C		

Notes:

1)All units available with either low cost solid state, or Transformer Isolated signal and reference inputs.

Transformer Isolation is highly recommended for all high voltage inputs, also when the signals are wired to more then one device or system, where ground loops or field noise may be significant for bus concerns, radar and antenna applications, and absolutely mandated for all Naval and most military concerns.

- 2)Accuracy applies over the operating temperature range, +/-10% amplitude and frequency variations, & +/-5% power supply variations.
- 3)Different input voltages and frequencies available, higher tracking rates and accuracy.
- 4)For all units any one input line may be grounded.



PCI-SYNCHROTMSERIES

OUTPUT CHANNELS

Proven "Whole" Std. Converters
Optimized Thermal Management
External Synchro Amp I/O,
100% Transformer Isolated I/O,
Permits 2 wire X/Y stators (Air)
☐ No External Parts or Power Req.'d
Reference Powered(NAVAL) Options
10-16 Bit Resolution Programable
Virtually Indestructable Outputs
1.2 to 4.5VA High Power Models
Mix/Match Input & Output Types
Simultaneous Fine/Coarse Transmit
For Multispeed Outputs

DESCRIPTION

The PCI-Synchro™ output channels are complete PCI to Synchro and Resolver output converters used for self-test, simulation, and control in military and industrial applications.

The PCI card is populated by 1-3 Industry Standard' Digital to Synchro and/or Digital to Resolver Converters, CT's or CDX's mixed as specified for the application.

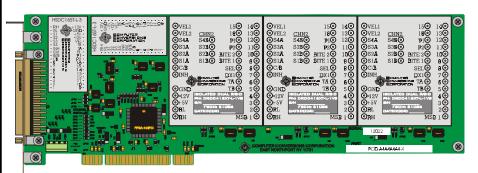
32 Bit long word level double buffered inputs are provided **on each channel** and addressable via independent or successive addresses.

The **true 32 Bit architecture** allows the converters to be written to as single word writes, **without** any fear of low-byte/high-byte **ambiguities**.

All of these converters feature virtually indestructible short-circuit proof outputs, overvoltage and transient protection, internal solid-plate heat sinks, and automatic thermal cutoff.

Complete transformer isolation is provided on all reference inputs and signal outputs to: eliminate ground loops, differing potentials, and to keep any high voltage transients from affecting the PCI backplane.

OPTIMUM THERMAL MANAGEMENT



VIRTUALLY INDESTRUCTABLE OUTPUTS

Both low cost "DSL/DRL Series" and reference powered "DSP Series" converters are offered to drive on-board loads of up to 4.5VA and external "booster amplifiers" are available to drive loads up to 150VA.

DSL/DRL TYPE CONVERTERS

The DSL/DRL series are low cost models that are powered from either ±15VDC or optionally ±12VDC supplies. The DC supply source may be field selected as sourced by an external input or Bus Powered from the PC/AT backplane. The power supplies should be verified as capable of providing the required current.

FEATURES

Proven "Whole" Std. Converters
Optimized Thermal Management
100% Transformer Isolated I/O,
No External Parts or Power Req.'d
Reference Powered Options
10-16 Bit Resolution
Virtually Indestructable Outputs
1.2 to 4.5VA Models
True 16 Bit Word Level Interface
Mix/Match Input & Output Types
Simultaneous Fine/Coarse
Transmit For Multispeed Outputs

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FUNCTIONS

Digital to Synchro/Resolver

Vector Generators (& DC Sine/Cos)

Solid State Control Transformers

Dual Channel Synchro Amplifiers

Isolated D-A's, Mod's/LVDT out's

Mix/Match w/ S-D/R-D's On-Board

Real Time, In-the-loop, Active

Control Differential's, CDX's Reference Powered D-S's.

Dynamic S/R & Vector Rotators

Standard ± 12 VDC units drive 1.2VA loads, and standard ± 15 VDC units drive 1.5VA loads. Higher drive models are available.

Frequencies of 400Hz. and higher require no external components, and two different types of output transformers are offered for 50-60Hz. units.

These units occupy one full size with a singlewidth slot.

Absolute value, vector units, "active" Differentials, Dynamic Rotators, & 2-10KHz. models, are available as standards for motor control apparatus and realtime simulators.

DSP TYPE CONVERTERS

DSP series 60Hz. units don't require any external transformers, and drive all 50, 60 & 400 Hz. loads.

The **DSP series** derives the output power from the reference (RH, RL) input and requires no ± 15 or ± 12 VDC supplies. This series features a very efficient, internal pulsating power supply that converts the reference input into a high-power, angle-weighted, synchro output format.

400Hz. **DSP** units **drive up to a** full 4.5VA load, and 60Hz. units drive a full 1.5VA load direct without requiring external output transformers

The DSP units occupy one full size doublewidth slot (DSP modules are .8"H).

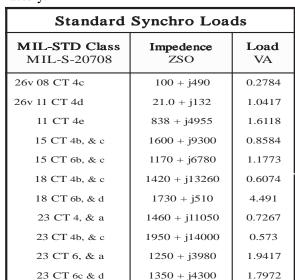
OTHER CONVERTERS

By looking at the model selection guide you will notice the PCI Series models, allow the choice of both Read and Write type Converters. Units with Isolated Digital to Analog, or LVDT converters, multispeed conversion, Vector Generators, Control Transformers and Differentials etc...can be configured by requesting "IBB Series Extended Model Selection Guide" from the factory.

DSL/DRL Units	DSL/DRL Units; Model Type, Drive/Load Verses Power Supply Load														
DC Power	-	+/-15	VDC	SU	PPL:	IES	+/-12VDC SUPPLIES								
Supplies]	Exte	rnal	+/-18	3VM	ax.	.Bus-Powered or Ext.								
Frequency	60	Hz. U	Jnits	400	Hz.	Units	60]	Hz. U	nits	400	Hz.	Units			
Model Type	**N	Std.	-3L	Std.	-3L	*-5L	**NL	Std.	-3L	Std.	-3L	*-5L			
Drive (VA)	0.02	1.5	2.2	1.5	2.9	5	0.025	1.2	1.7	1.2	2	3.4			
90V. Synchro in Kohms		4	2.7	4	2	1.2		5	3.5	5	3	1.78			
11.8V. Syn in ohms				70	36					87	52				
11.8V. Res in ohms				93	48					116	70				
Avg. DC Current (ma.)	120	150		150			150	220		200					
Avg.Peak Current(ma.)	120	330		330			150	485		440					
Foldback (ma.)	120	600		600		2000	180	600		600		2000			

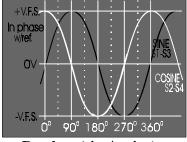
Notes:1)** These units used to power external synchro power amplifiers, upto 300VA,

- 2)* These units require double slot assy. for converter height & thermal considerations.
- 3) All units require sufficient forced air cooling, thermal cut-off @125C auto-resume.
- 4) 60 Hz. units require an external transformer, see dwg.below, for +/-15VDC powered units use P/N DSC60-15, for +/-12V powered units use P/N DSC60N.
- 5)The +/-12 or +/-15VDC supplies should feature fold-back current limmiting to insure that they gradually increase the voltage with load surge causede during power on. Most reasonable supplies (including most switchers) features feature this.
- 6) Both the + and supplies should power up simultaneously to minimize power surges (typical of all class B amps.) Tracking supplies should be considered where practicle.



Notes: 1) 6 = 60 Hz., 4 = 400 Hz. units 2) 26V = 26Vsystem, 11.8VL-L signals else; 115Vsystem, 90VL-L signals typ

60°120°180°240°300°360° 30° 90° 150° 210° 270° 330'

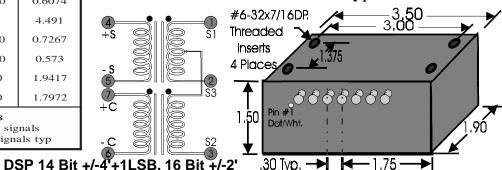


Synchro Signals

Resolver (sine/cosine)

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50/60 Hz. Transformer P/N DS60N For DSL/DRL Type 50/60 Hz. units



Accuracy: DSL Type

12 bit units +15 arc minutes

14 bit units +4 arc minutes

accuracy applies over operating temp. range, ±10% amplitude & frequency

variations, ±5% variation power supplies, ±10% harmonic distortion

16 bit units +/-2.3 arc minutes, -HA units +/-1' +1LSB,

* Models upto 20KHz. available









PCI-SYNCHROTM SERIES MODEL SELECTION GUIDE

Model:

PCI B 4 K 4 K 4 X12 **OPTIONS** a b a b a b a b CH.0 CH.1 CH.2 CH.3

SELECT BASE CARD STYLE::

PCI = EITHER INPUT OR OUTPUT TYPES

*PCID = HIGH DENSITY CARDS

* Note: PCID Series High Density Cards Use 2 Input Converters per Input Channel Selection.

1st.	Select	base Mo	odel: PCI o	r PCID							
2nd.	Select Function/Resolution code "table a) below "A-U"										
3rd.	Select Signal Code "table b)" to your right "1-9"										
4th.	Add Additional channels or "00" for none										
5th.	Add an "X" for ISOLATION and options										
a) *****	a) **********RF,\$OLUTION*********										
BITS	16	14	12	10							
	Table a) Resolutionn/ Function										
SYNCHRO '	TO DIGI	TAL (SDC) TYPE								
Code	Α	В	C	D							
RESOLVER	TO DIG	ITAL (RD	C TYPE)								
Code	Е	F	G	Н							
DIGITAL TO	O SYNCE	IRO (DSL)	TYPE								
Code	J	K	M	N							
DIGITAL TO	O SYNCH	IRO (DSP)	TYPE REF.	PWRD							
Code		P									
DIGITAL TO	O RESOL	VER (DRI	L) TYPE								
Code	R	S	T	U							
ALL OTHE	RS REQU	EST EXTE	NDED MODI	EL GUIDE							

b) SELI	b) SELECT SIGNALS (INSERT CODE #)								
REFERENCE VOLTAGE	SIGNAL LEVELS	FREQUENCY IN HERTZ	INSERT THIS CODE						
26VAC	11.8V. L-L	400 Hz.	1						
26VAC	11.8V. L-L	2.6KHz.	2						
26VAC	26V. L-L	400 Hz.	3						
115VAC	90V. L-L	400 Hz.	4						
115VAC	90V. L-L	60 Hz.	5						
*115VAC	*7V. L-L	400Hz.	6						
*115VAC	*7V. L-L	60 Hz.	7						
*26VAC	*7VL-L	400Hz.	8						
ALL OTHERS	S REQUEST	EXTENDED MO	DEL GUIDE						
NOTES: * 1) These Converters typically used to drive power amplifiers, 6V. L-L with +/-12V. bus power									

Example: Model; PCI-B4B4K4K4-X12

Includes: 1 PCI 2.2 Compliant Card,

Populated with 2 14 Bit S-D Converters (B4B4)

and, 2 14 Bit D-S Converters (K4K4)

All Reference inputs 115VAC @ 400Hz. (B4/K4)

All the Signals are 90V.L-L @ 400Hz. (B4/K4)

Reference & Signals Transformer Isolated (X)

 ± 12 VDC may be powered by the PCI Bus. (12)

Options: ADD:

- -8 For Independent References on PCID's -HS For High Speed
- -GA or HA Accuracy
- X For Transformer Isolation
- ±12 verses ±15VDC Power Inputs, -V For DC Velocity Outputs -12 For (+12 for Bus Powered)
- -M For Multispeed Operation
- -Q Quadrature Incremental Encoder Outputs | -WR For With Reference Supplies
- 1 For O^oC to +70^oC Oper. Temp.
- 3 For -40°C to +85°C Oper. Temp.

Typical Cable:

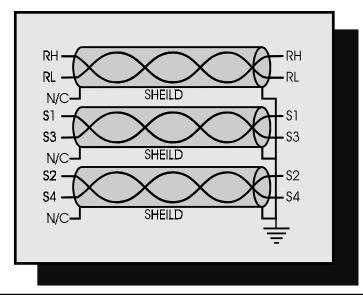
Environmental Specifications:

Temperature: Operating -1) 0 - 70°C, $0 - 60^{\circ}$ C installed, -2)-55 to $+105^{\circ}$ C -3)-40 to +85°C/Storage: -55 to +125°C **Humudity**: 0 to 95% (non-condensing)

Vibration: 3.5 mm. 5-9Hz. :

1.0 G 9 - 150Hz.

15 g's for 11 msec. Shock:



Notes:

- 1)Screw circuit card mounting bracket to chassis prior use.
- 2)All shields to go direct to Earth Ground on the computer side only.
- 3)To reverse direction of rotation swap S1 with S3 or invert data in software.
- 4)S4 is not used on Synchro units.
- 5)J9 and J10 jumpers are installed on Bus-Powered units. They must be removed if external ± 12 or ± 15 VDC supplies are being applied to the connector. See Picture showing address jumpers for J9, J10 locations.
- 6)When using non-reference powered D-S/D-R con verters or cards with internal reference supplies; make sure supply provides ample -12 or -15VDC depending on model.
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			PIN TEI	RMINA	ATIONS:	PCI Se	eries Uni	ts.		
PIN #		ERIES High Density	D-S/R Amp. Support	S/R-D QM Option	QM			ERIES High Density	D-S/R Amp. Support	S/R-D QM Option
38	RH	Deference		RH	I	46	RH	D - f		RH
5	RL	Reference		RL	1	13	RL	Reference		RL
36	S1			S1		44	S1	Signals]	S1
3	S2	Signals		S2		11	S2	Channels		S2
19	S3	Channels 0		S3	1	27	S3	2		S3
35	S4			S4	1	43	S4			S4
21	S1		DISABLE	B-	1	29	S1		DISABLE	B-
37	S2	Signals	BIT	B+	1	45	S2	Signals	BIT	B+
4	S3	Channels	GND.	A-	1	12	S3	Channels 6	GND.	A-
20	S4	4		A+	1	28	S4	O		A+
18	OPT	*V0:RL4 *V4::RH4		M+	1	26	OPT	*V2::RL6	1	M+
2	OPT.			M-	1	10	OPT.	*V6:RH6	1	M-
42	RH	Reference		RH		50	RH	Reference	1	RH
9	RL			RL	1	17	RL]	RL
40	S1			S1		48	S1	a		S1
7	S2	Signals		S2	1	15	S2	Signals Channels		S2
23	S3	Channels		S3	1	31	S3	3		S3
39	S4			S4	i	47	S4			S4
25	S1	a	DISABLE	B-		33	S1	a	DISABLE	B-
41	S2	Signals Channels	BIT	B+	1	49	S2	Signals Channels	BIT	B+
8	S3	5	GND.	A-		16	S3	7	GND.	A-
24	S4			A+		32	S4			A+
22	ODT	*V1::RL5		M+		30	OPT	*V3::RL7		M+
6	OPT.	*V5::RH5		M-		14	OPT.	*V7::RH7		M-
1		DC CON	MMON	-		34		DC CON	MMON	•
		IF M I	II TISPEED: 1	Pair Fin	ne/Coarse	e Chan	nels: 0/1	2/3 4/5 6/7		

IF MULTISPEED: Pair Fine/Coarse: Channels: 0/1, 2/3, 4/5, 6/7.

J3 Connector AMPHENOL# DDH-50SAM4 Mate Souriau # D50P 032N MIL M24308/3-5 50 Pin D



